

37 CFR 1.8 CERTIFICATE OF MAILING

I hereby certify that this correspondence and any documents indicated as being enclosed therein are being deposited with the U.S. Postal Service for first class mail in an envelope addressed to: Official Draftsperson, Assistant Commissioner for Patents, Washington, D.C. 20231, on

Date:

4/12/2001

Signature

Printed Name: Stephanie Klepp

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of:

JOHN S. RHOADES

Serial No. 09/751,720

Filed: December 29, 2000

Group Art Unit: 2131

Examiner: Not Assigned

Attorney Docket No. 99-039-TAP

For: SECURE GATEWAY MULTIPLE AUTOMATED DATA STORAGE SYSTEM SHARING

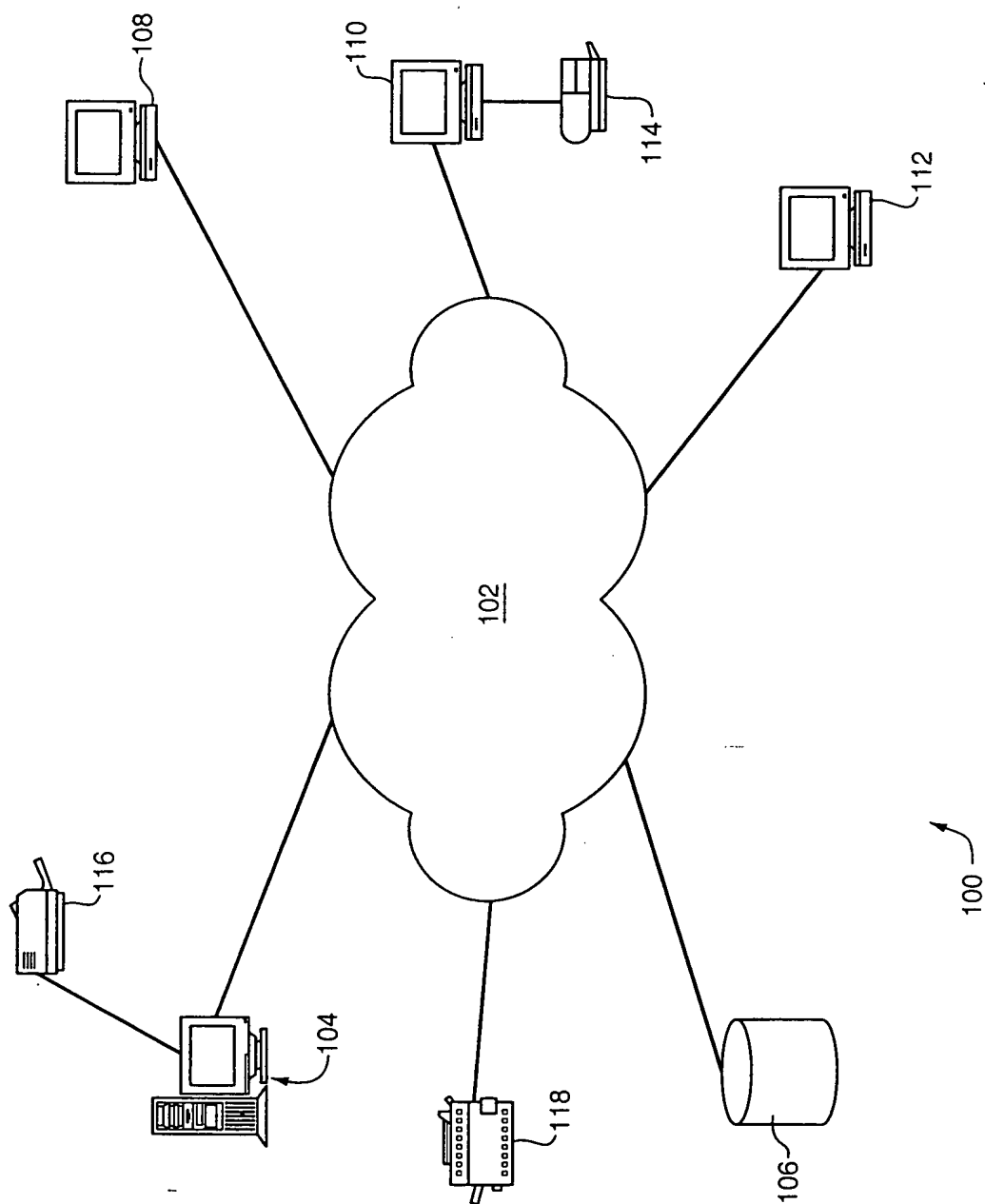
FORMAL DRAWINGS TRANSMITTAL

ATTN: OFFICIAL DRAFTSPERSON  
ASSISTANT COMMISSIONER OF PATENTS  
Washington, D.C. 20231

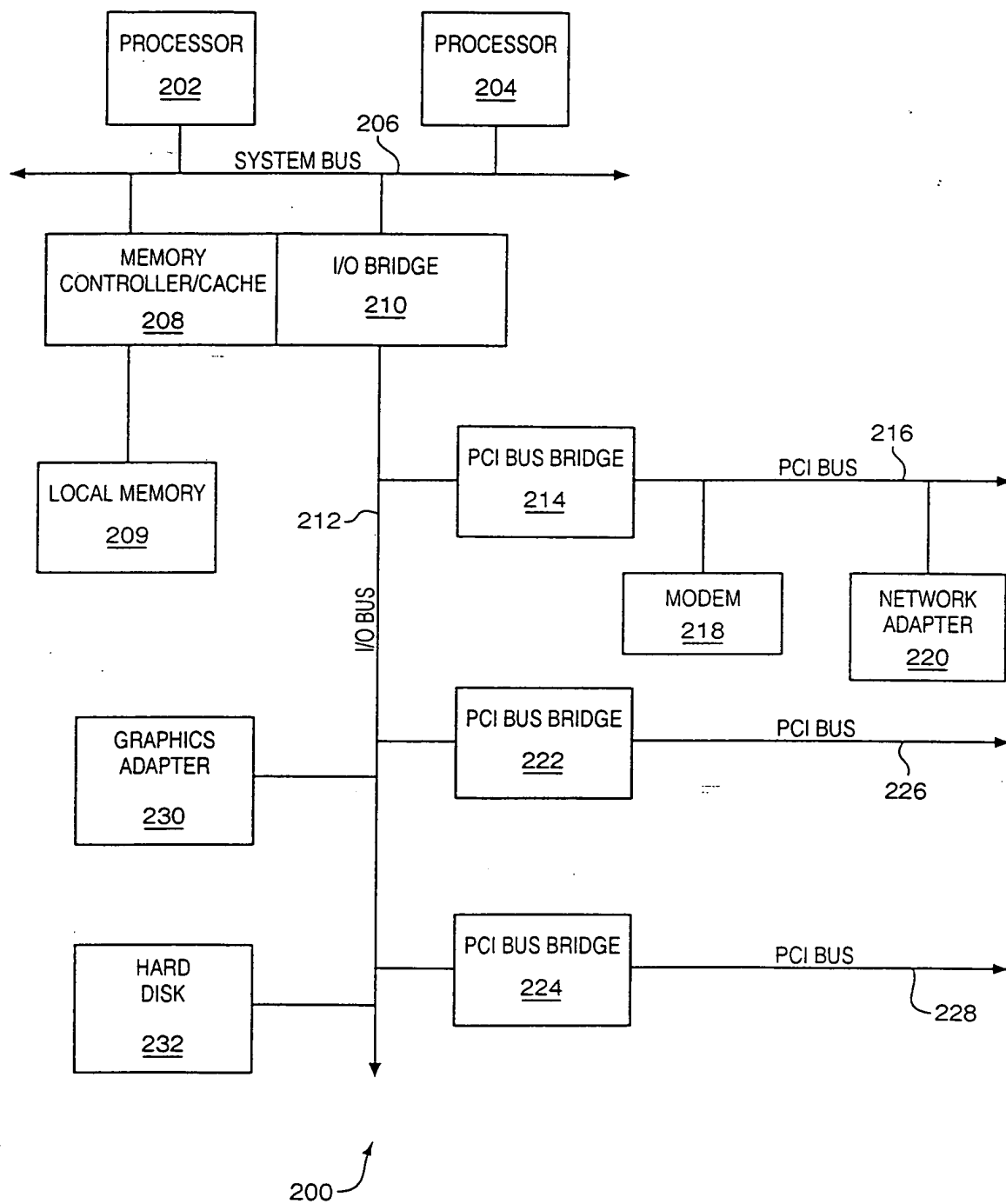
Enclosed herewith are the formal drawings (nine (9) sheets; Figures 1-9) in the above-captioned patent application.

Respectfully submitted,

Wayne P. Bailey, Reg. No. 34,289  
Storage Technology Corporation  
One StorageTek Drive, MS-4309  
Louisville, Colorado 80028-4309  
(303) 673-8223



**FIG. 1**



**FIG. 2**

FIG. 3 is a block diagram of a computer system 300. The system includes a processor 302, a host/PCI cache/bridge 308, main memory 304, and a bus 306. The bus 306 is connected to various adapters and interfaces, including a SCSI host bus adapter 312, a LAN adapter 310, an expansion bus interface 314, a graphics adapter 318, an audio/video adapter 319, and an audio adapter 316. The SCSI host bus adapter 312 is connected to storage devices such as disk 326, tape 328, CD-ROM 330, and DVD 332. The expansion bus interface 314 is connected to a keyboard and mouse adapter 320 and a modem 322. The audio/video adapter 319 is connected to memory 324.

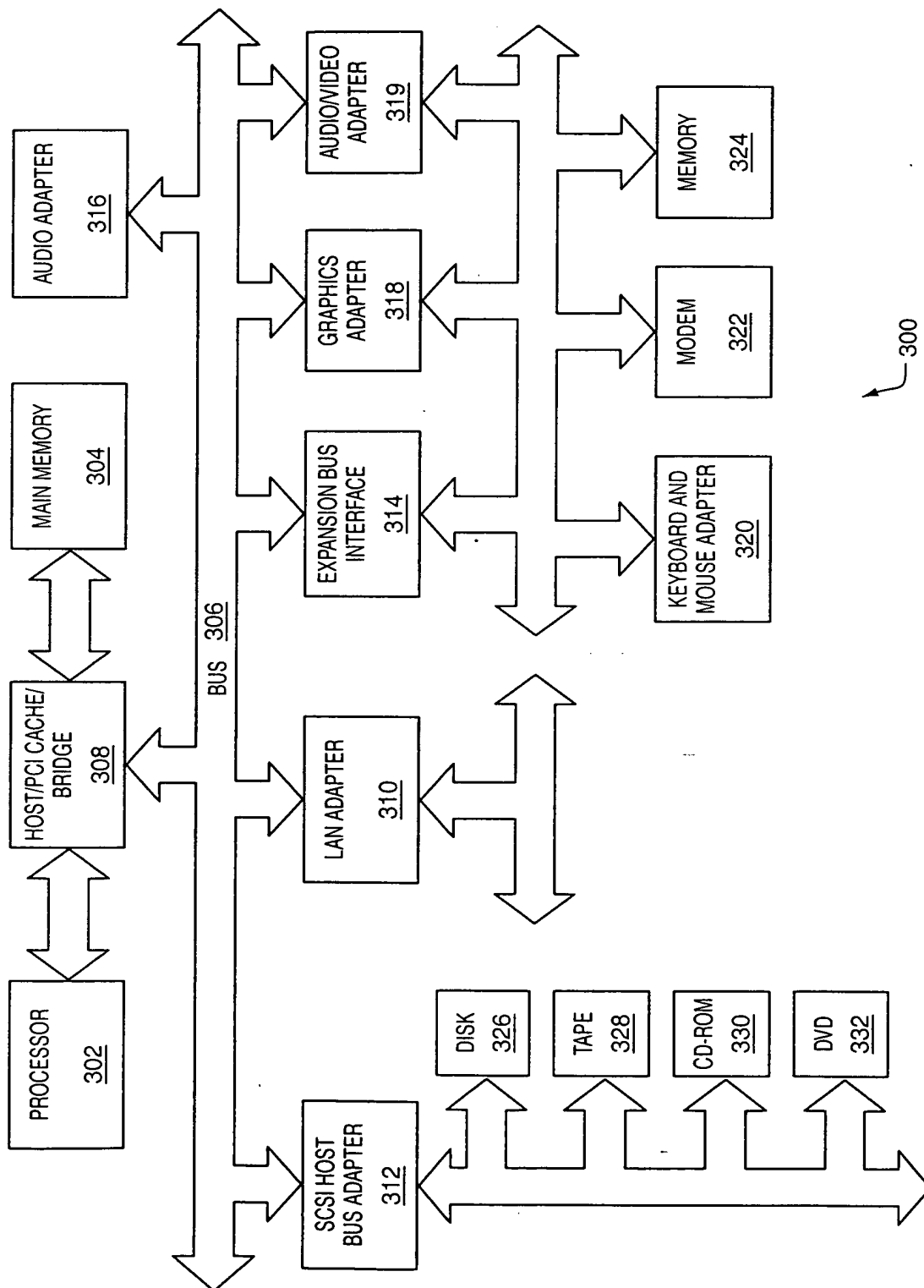
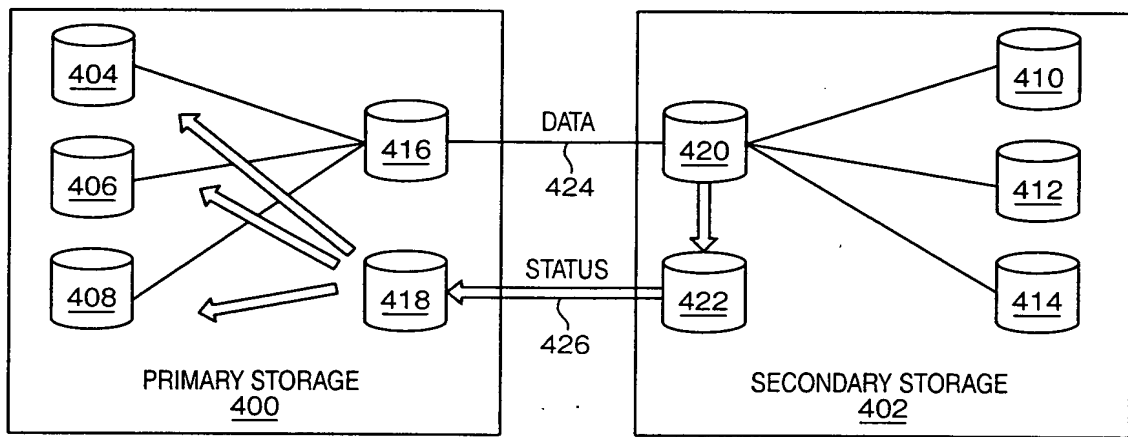


FIG. 3



**FIG. 4**

**FIG. 5**

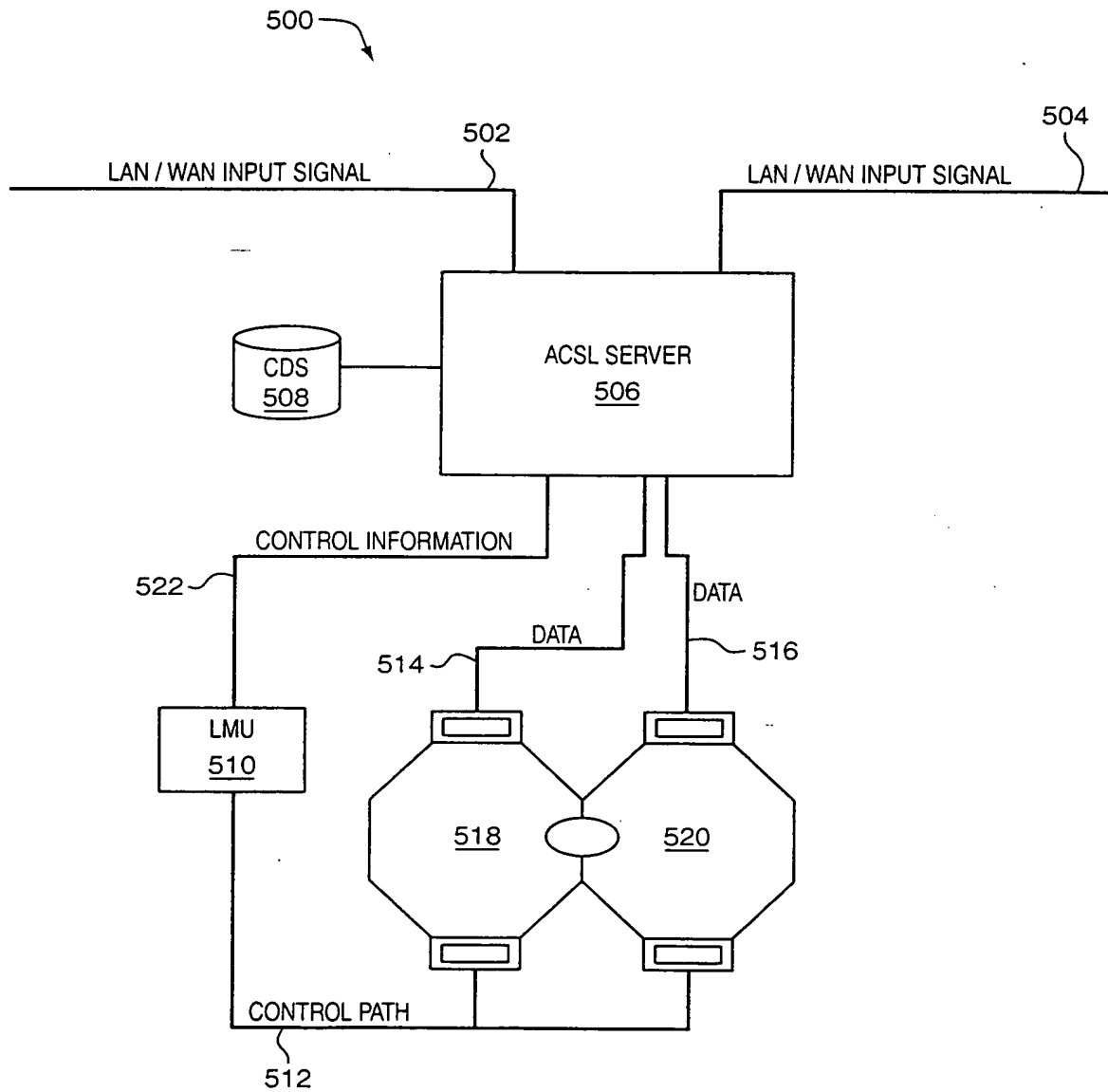


FIG. 6

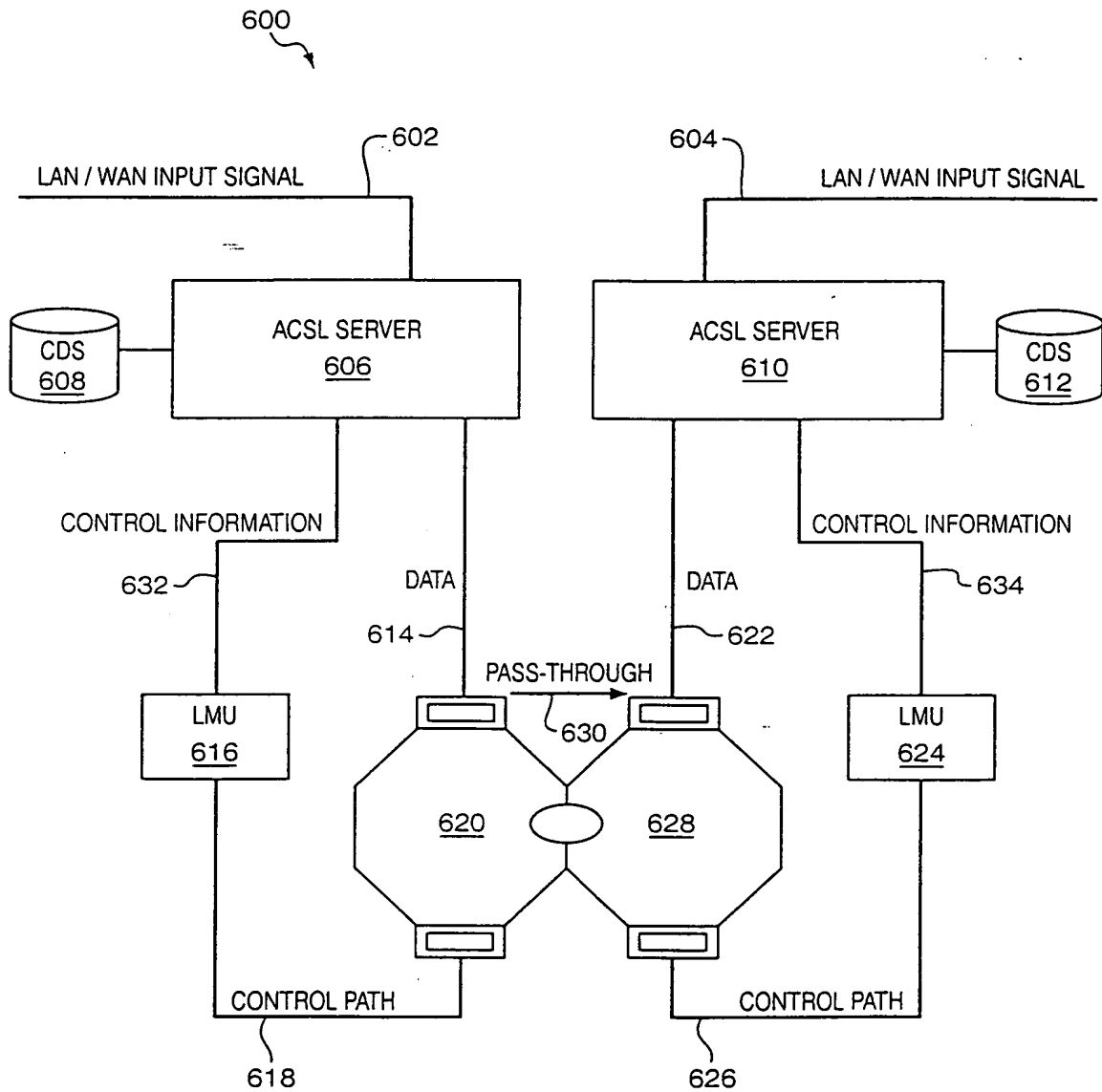
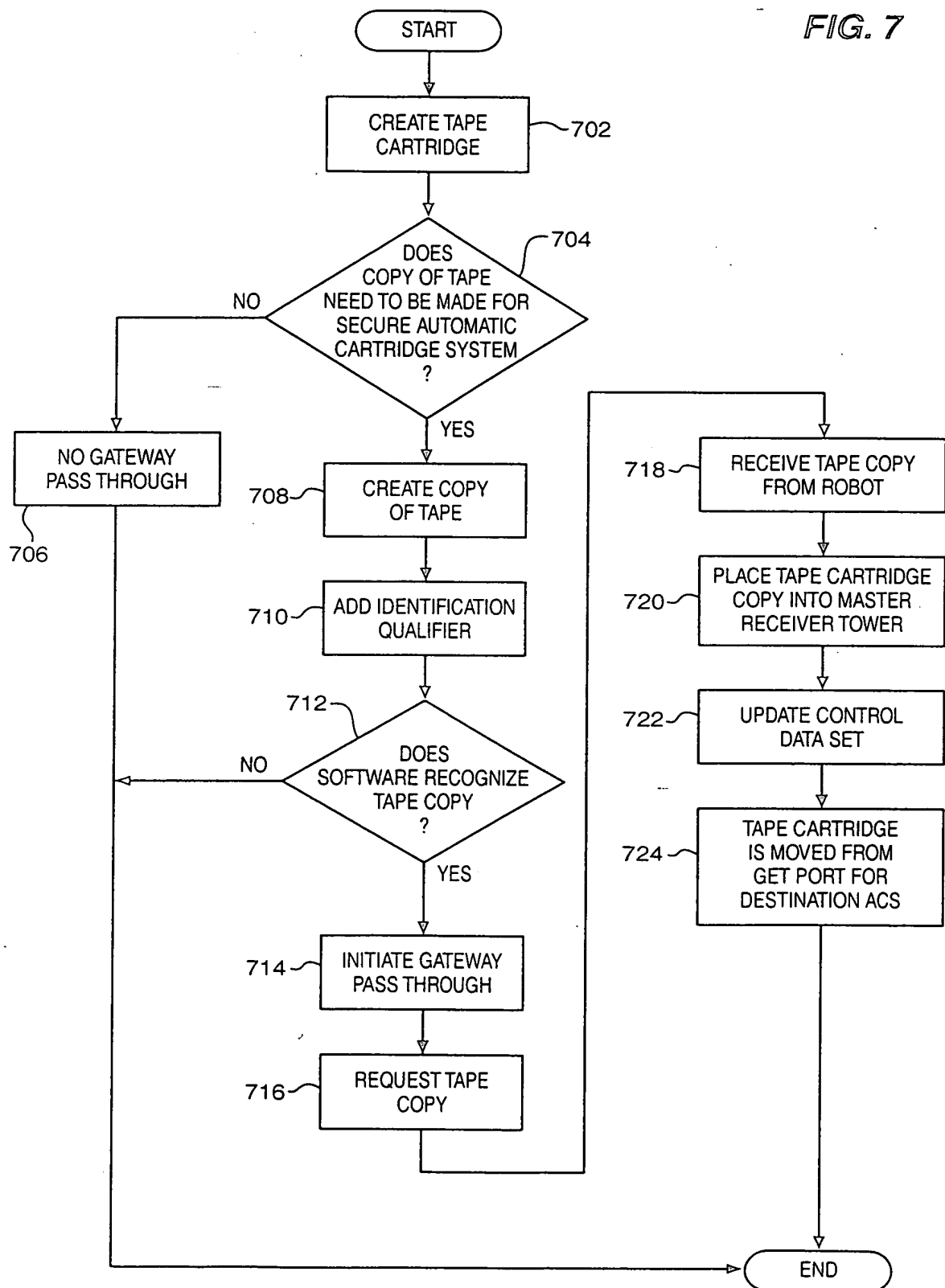


FIG. 7





**FIG. 8**

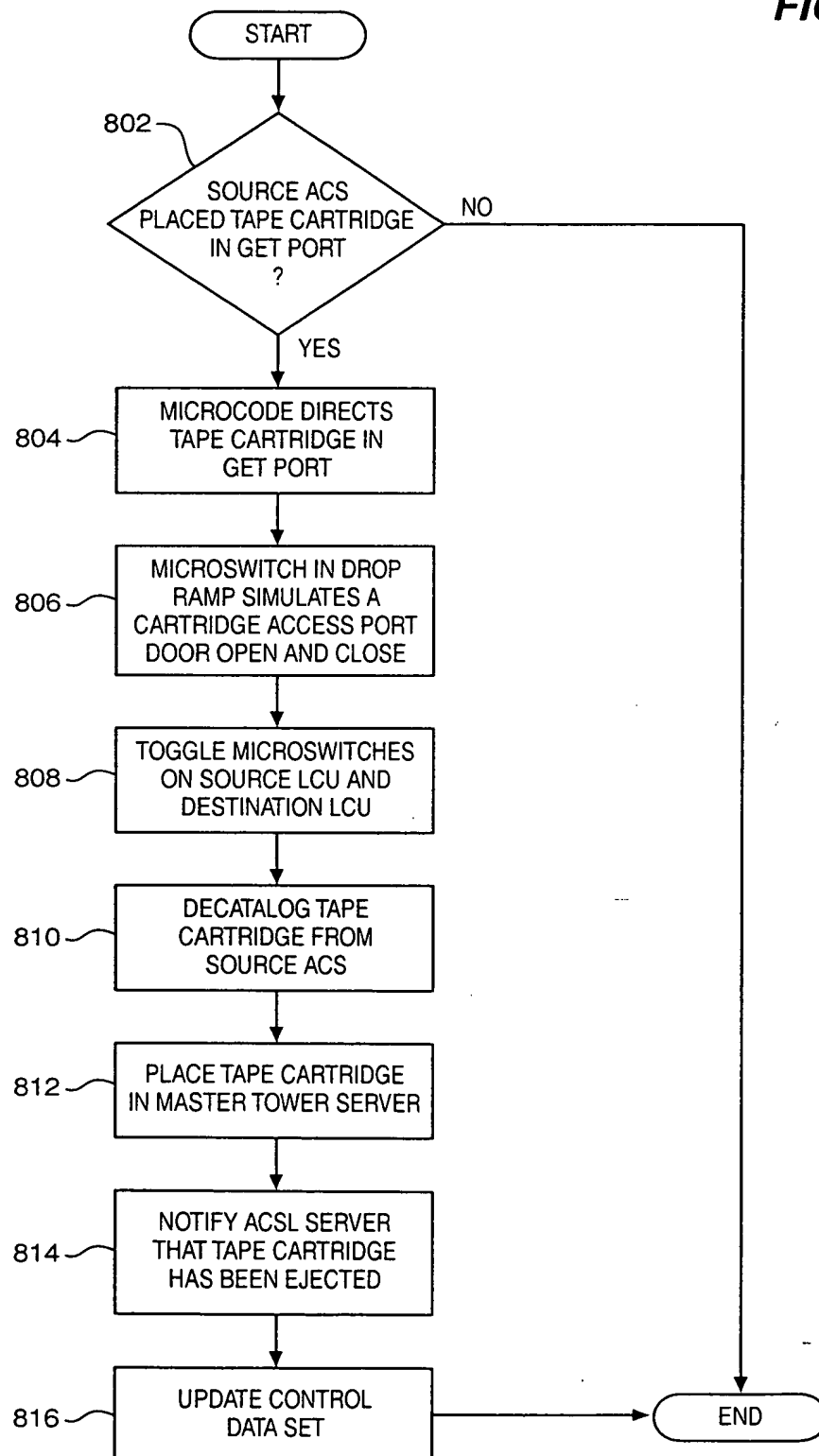


FIG. 9

